

APPLICATION DATA SHEET

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Title of Invention: METHOD OF FABRICATING A MOS TRANSISTOR WITH LOW GATE DEPLETION

Customer Number Attorney: 027765



Customer Number Correspondence Address: 027765



INVENTOR(s):

Primary Citizenship: R.O.C.

Given Name: Kent Kuohua

Family Name: Chang

Residence City: Taipei City

Residence Country: TW

Address:
4F, No. 66-1, Yen-Chi St., Taipei City, Taiwan, R.O.C.
Taipei City , TW